



PATENT

Case Docket No. CSPTAL15.004AUS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) : Yan et al.  
Appl. No. : 10/618,544  
Filed : July 11, 2003  
For : ORGANIC THIN FILM  
TRANSISTOR (OTFT) AND  
MANUFACTURING PROCESS  
THEREOF  
Examiner : Unassigned  
Group Art Unit : Unknown

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on

10/6/03

(Date)

John M. Carson, Reg. No. 34,303

TRANSMITTAL LETTER

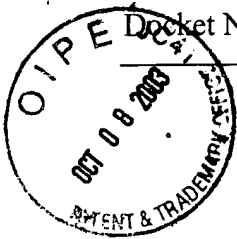
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Enclosed for filing in the above-identified application are:

- (X) An Information Disclosure Statement.
- (X) A PTO Form 1449 with four references.
- (X) The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment, to Account No. 11-1410.
- (X) Return prepaid postcard.

John M. Carson  
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Attorney of Record  
Customer No. 20,995  
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**INFORMATION DISCLOSURE STATEMENT**

Applicant : Yan et al.  
App. No. : 10/618,544  
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For : ORGANIC THIN FILM TRANSISTOR  
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Examiner : Unassigned  
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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Enclosed is form PTO-1449 listing four references that are also enclosed. The PCT publication No. WO 99/40631 is a corresponding international application of Chinese Patent Application No. 1293825A.

This Information Disclosure Statement is being filed with an RCE or within three months of the filing date of this application and no fee is required in accordance with 37 C.F.R. § 1.97(b)(1), (b)(2), or (b)(4).

Respectfully submitted,  
KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: 10/6/03

By: \_\_\_\_\_  
John M. Carson  
Registration No. 34,303  
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(619) 235-8550

FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.  
CSPTAL15.001AUSAPPLICATION NO.  
10/618,544INFORMATION DISCLOSURE STATEMENT  
BY APPLICANT

(USE SEVERAL SHEETS IF NECESSARY)

APPLICANT  
Yan et al.FILING DATE  
July 11, 2003GROUP  
Unknown

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	1	WO 99/40631	8/12/99	PCT				
	2	1293825A	2/5/01	CN				

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)	
	3	C. D. Dimitrakopoulos, et al., "Low-Voltage Organic Transistors on Plastic Comprising High-Dielectric Constant Gate Insulators" (1999) Science Vo. 283, pgs 822-824.
	4	H. Klauk et al., "A Reduced Complexity Process for Organic Thin Film Transistors" (2000) Applied Physics Letters, vol 76, No. 13, pgs 1692-1694.

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EXAMINER	DATE CONSIDERED
*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.	